

Abstract of the Disclosure

In an LCD apparatus, an LCD panel receives an image data from a data driver and displays an image in response to a gate driving signal from a gate driver. A timing controller provides a first control signal to the gate driver so as to control an output of the gate driving signal and provides a second control signal to the data driver via a signal line formed on the LCD panel so as to control an output of the image data. Accordingly, the gate driving signal and image data may be applied to a corresponding pixel area at the same time. Also, the LCD apparatus may prevent deterioration of the image, which is caused by applying-time difference between the gate driving signal and image data, thereby improving the display quality thereof.